

# EV182655314

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No
Filing Date May 3, 2001
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Arroup Art Unit
Examiner Laura M. Schillinger
Attorney's Docket No
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

## **RESPONSE TO OCTOBER 24, 2002 OFFICE ACTION**

To:

**BOX AMENDMENT** 

**Assistant Commissioner for Patents** 

Washington, D.C. 20231

From:

Deepak Malhotra (Tel. 509-624-4276; Fax 509-838-3424)

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## **AMENDMENTS**

### In the Claims

Please replace the claims with the following clean version of the entire set of pending claims, in accordance with 37 C.F.R. § 1.121(c)(1)(I). Cancel all previous versions of any pending claim.

A marked up version showing amendments to any claims being changed is provided in one or more accompanying pages separate from this amendment in accordance with 37 C.F.R. § 1.121(c)(1)(ii). Any claim not accompanied by a marked up version has not been changed relative to the immediate prior